



Charge Control M Errata

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Revisions

Revision	Release Date	Changes
1	September 06, 2019	initial release

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This document details all known errata for Charge Control M.

1 Double use of MAC addresses

1.1 Description

The production process of Charge Control M allowed that boards with the same MAC address and serial numbers were produced and tested ok. When finding the error the production and delivery was stopped immediately - it was possible to determine the total number of defective boards. 230 boards with non-unique MAC addresses and serial numbers were delivered to customers.

1.2 Affected Devices

230 devices with MAC addresses in the range of 00:01:87:05:95:D3 to 00:01:87:05:A1:D0 and serial numbers in the range of 60939 to 61962 are affected by this errata. The delivery date of the boards is between April 2019 and August 2019.

1.3 Impact

Network communication might be affected if two boards with the same MAC address are working within the same network at the same time.

1.4 Workaround

In case two board were found with the same MAC address, please integrate them in separate networks.

1.5 Fix plan

I2SE improves the quality control process in order to avoid this issue.